

Fig.1A

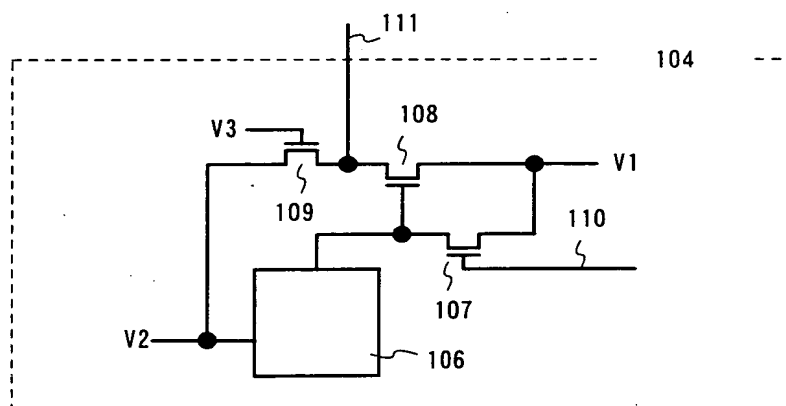


Fig.1B

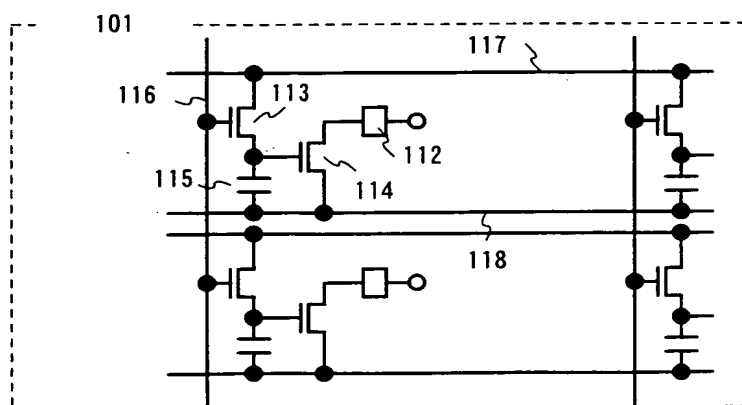
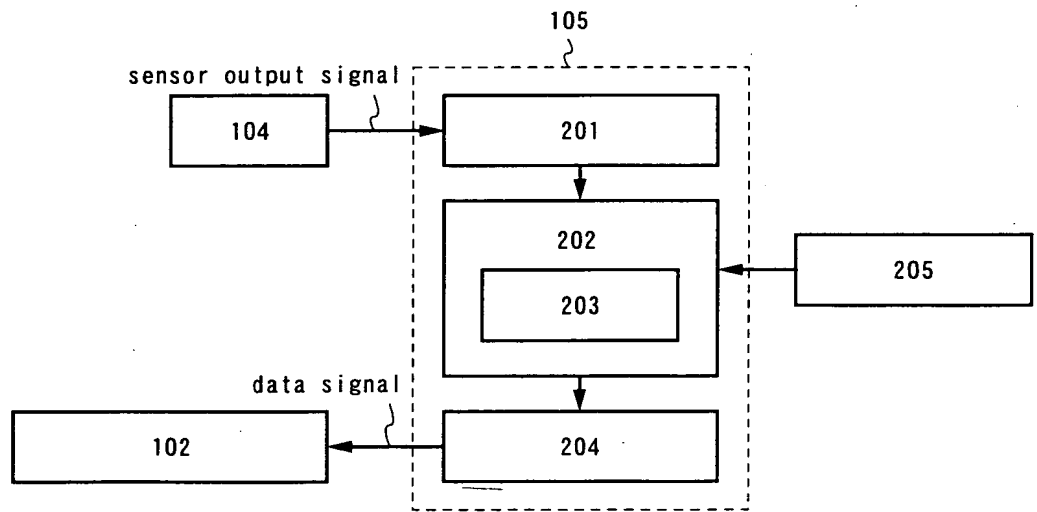


Fig.1C

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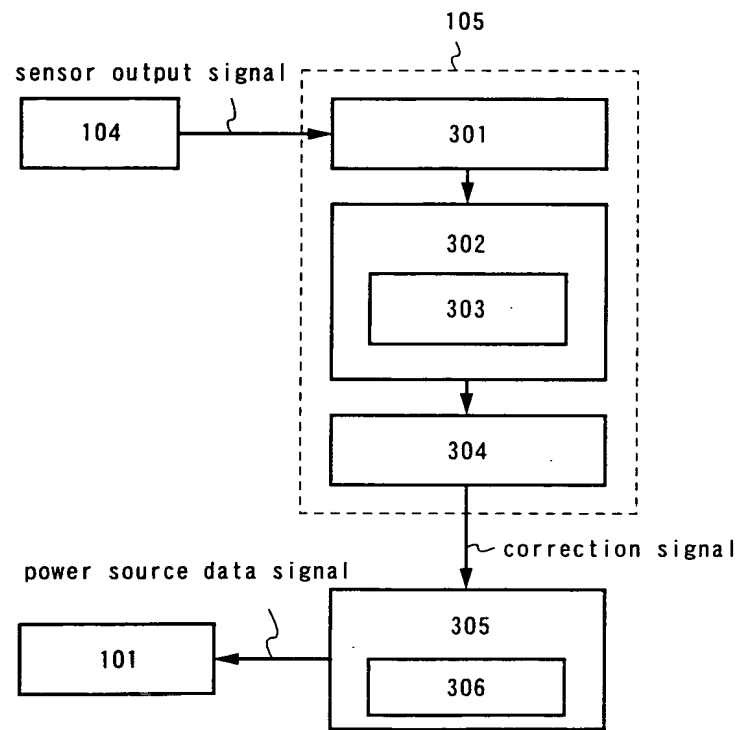
Fig.2



Analog Driving System

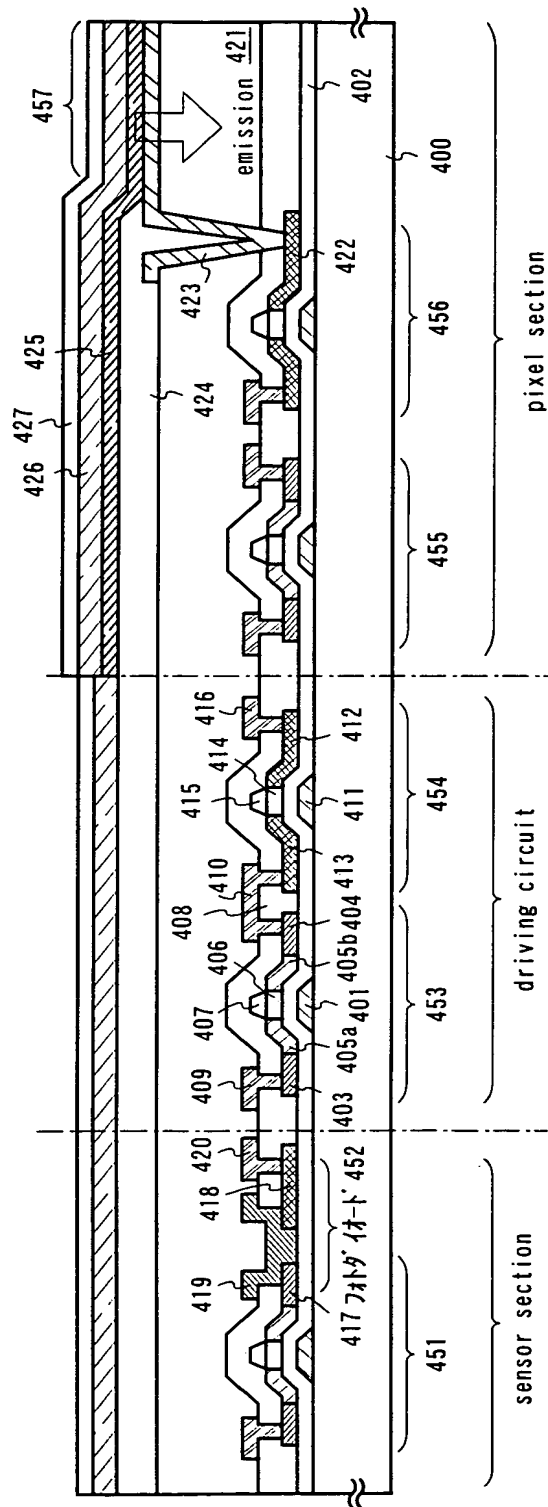
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Fig.3



Digital Driving System

Fig.4



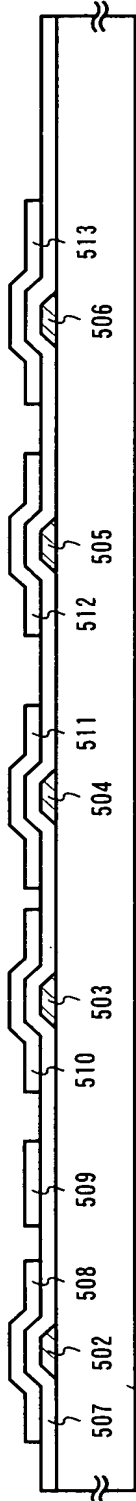


Fig. 5A

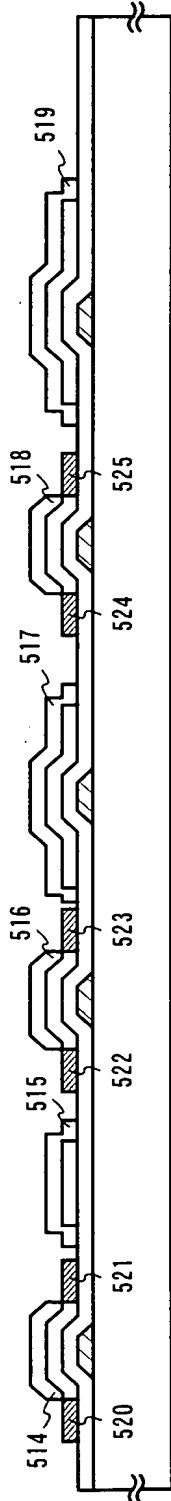


Fig. 5B

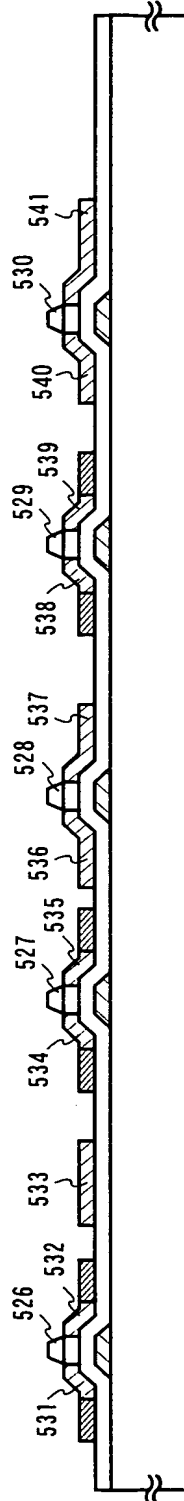


Fig. 5C

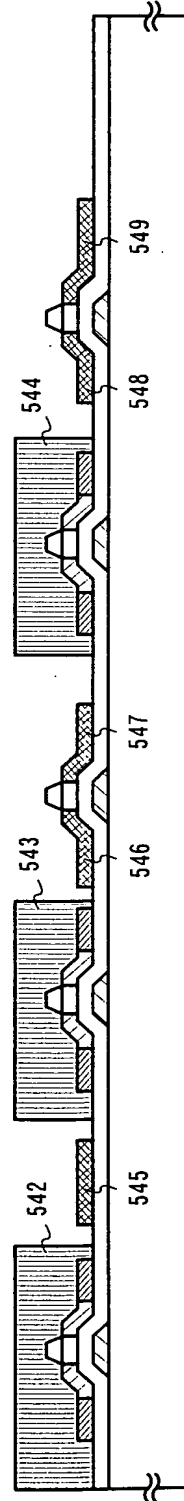


Fig. 5D

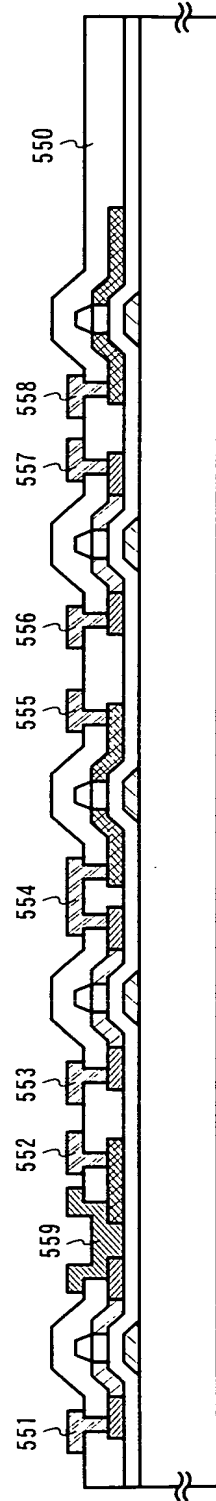


Fig. 5E

Fig.6

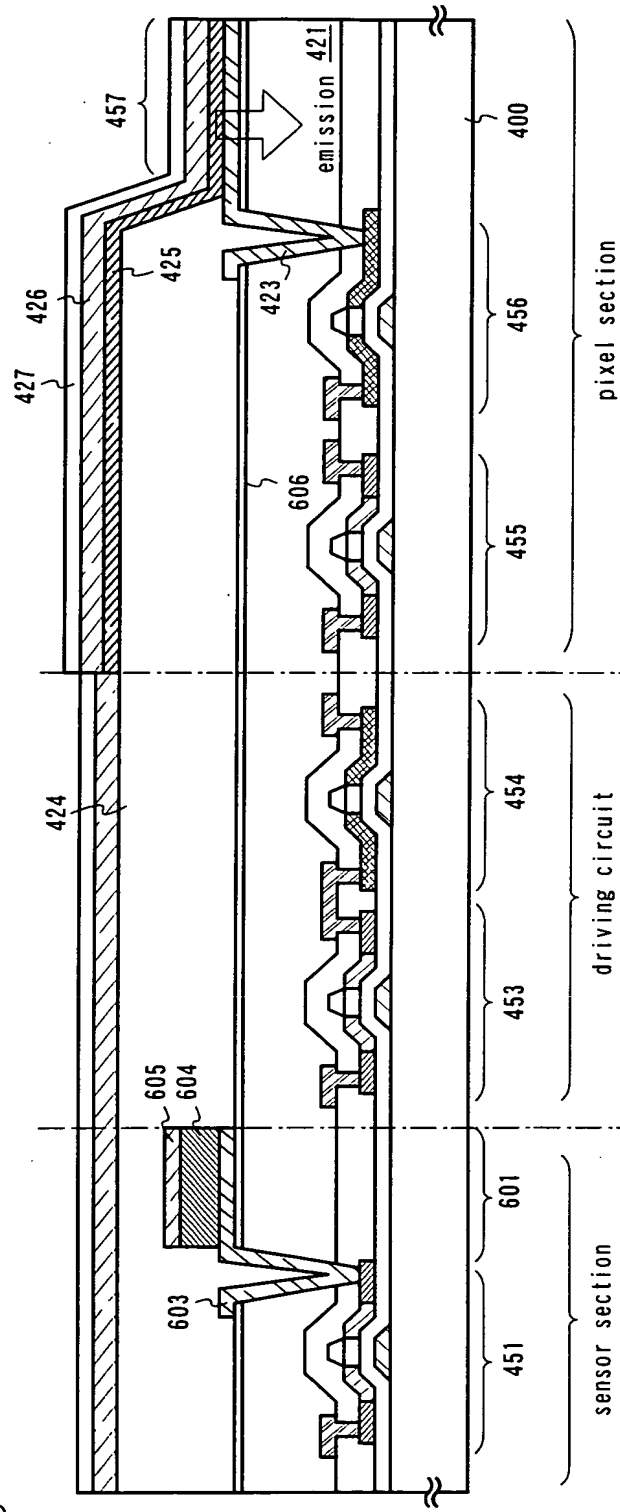


Fig.7

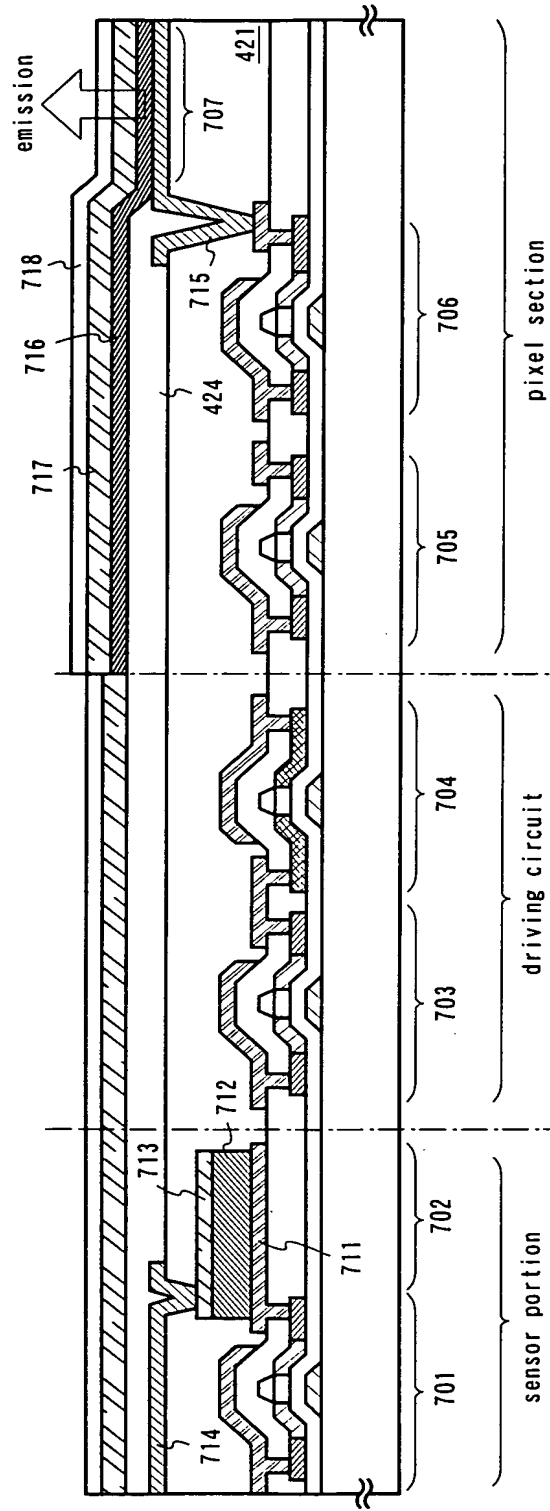


Fig.8

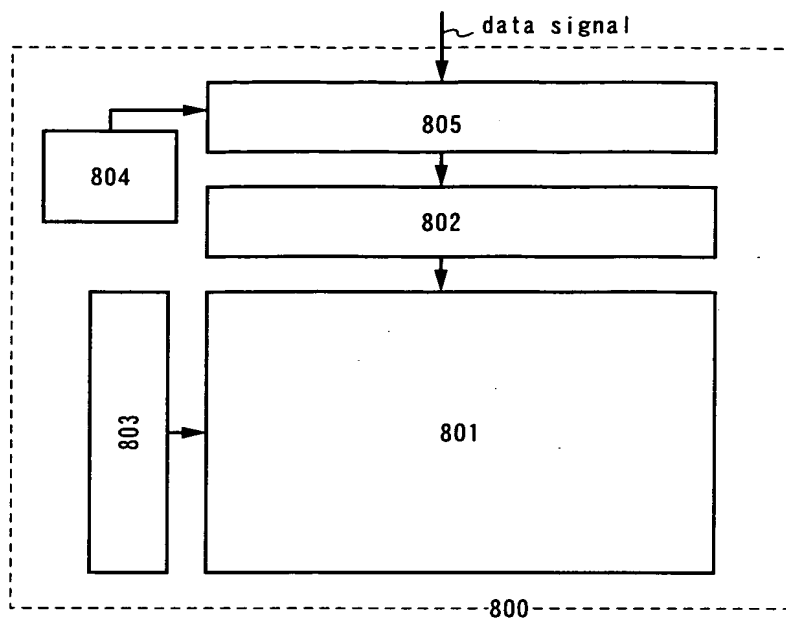


FIG. 8



Fig.9A

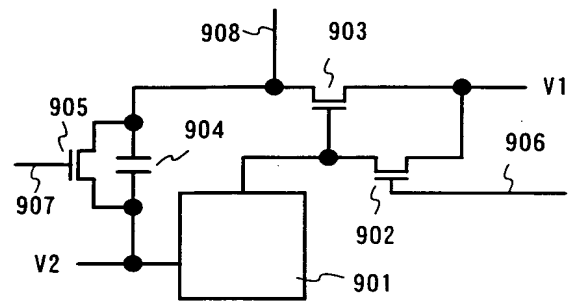


Fig.9B

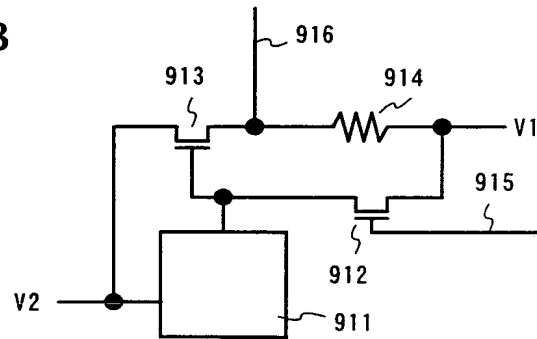
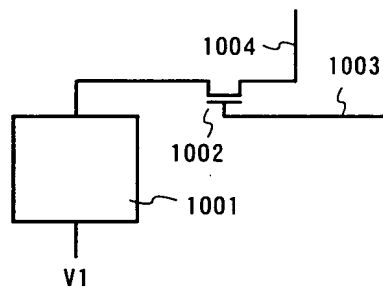
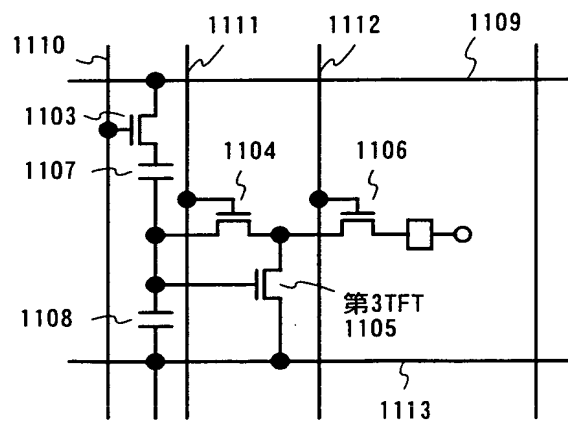
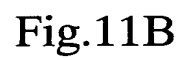


Fig.10



**SECRET - DECLASSIFIED**



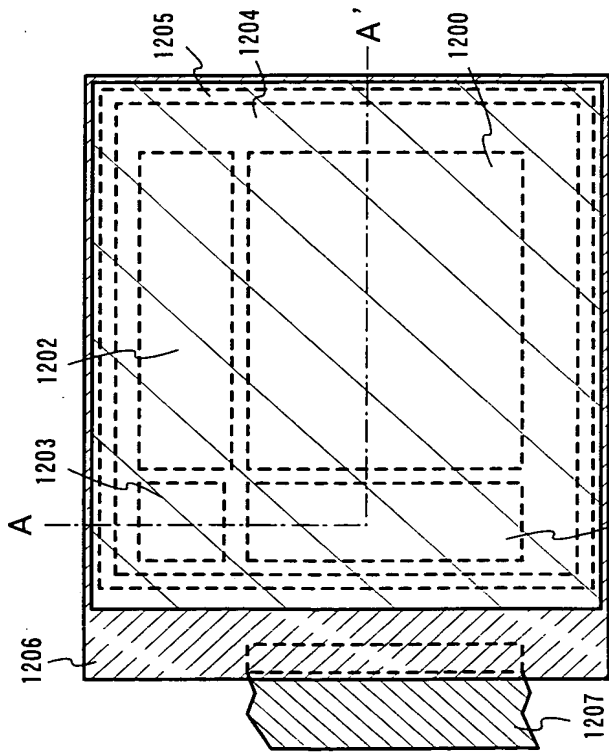


Fig.12A

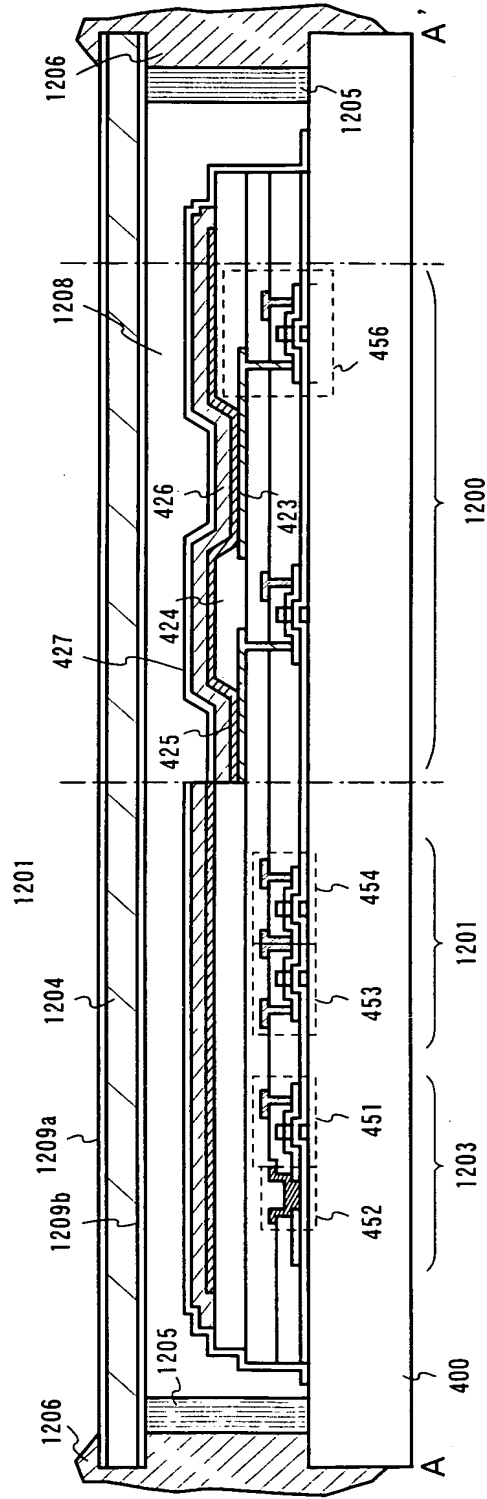


Fig.12B

Fig.13A

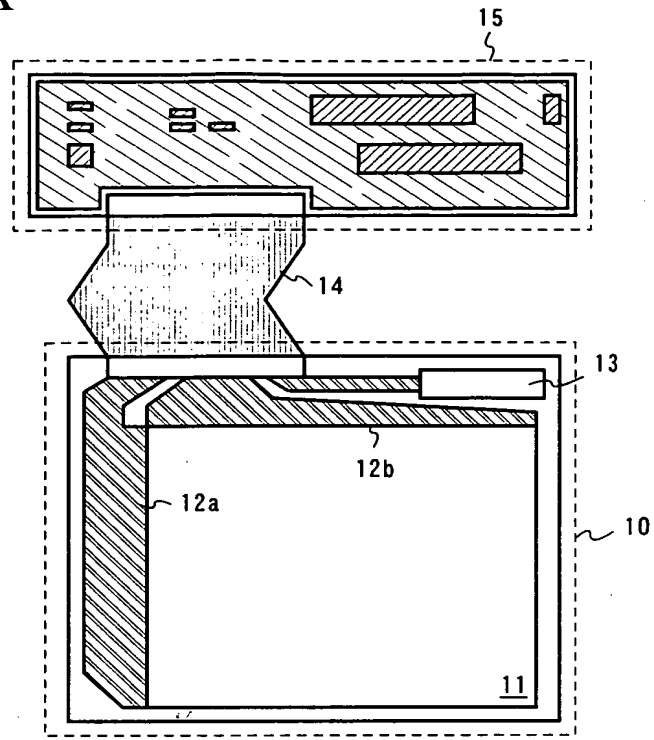
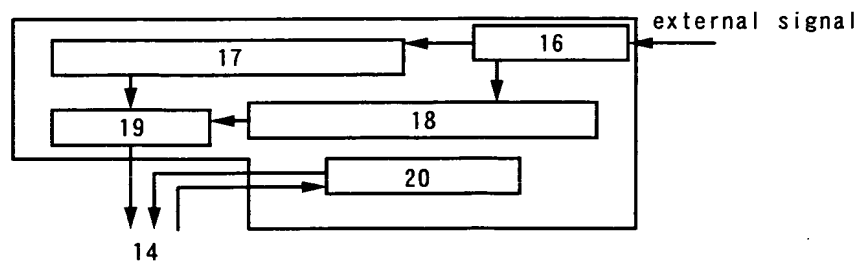


Fig.13B



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Fig.14A

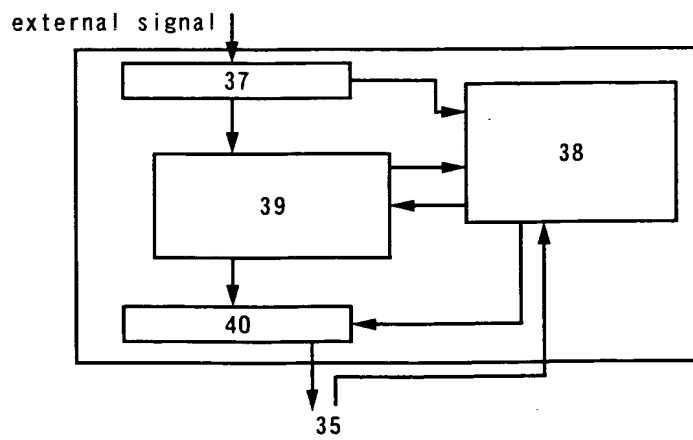
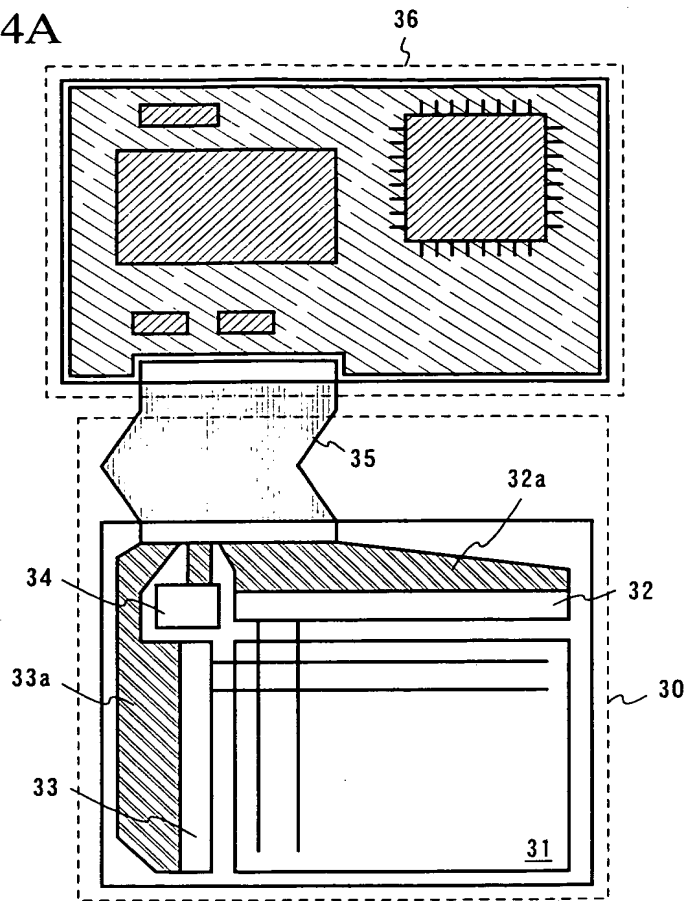


Fig.14B

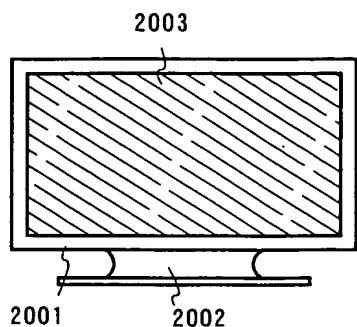


Fig.15A

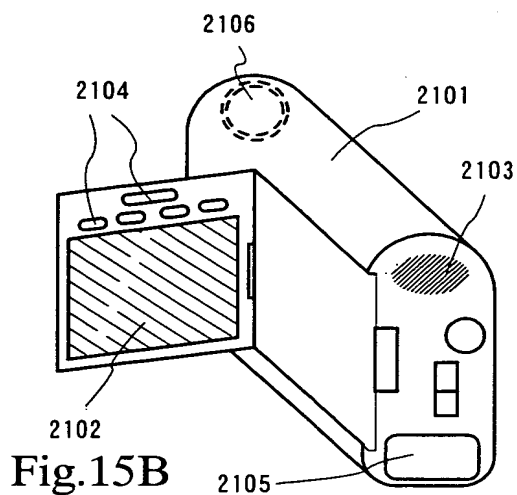


Fig.15B

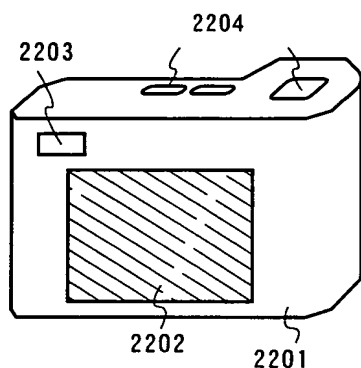


Fig.15C

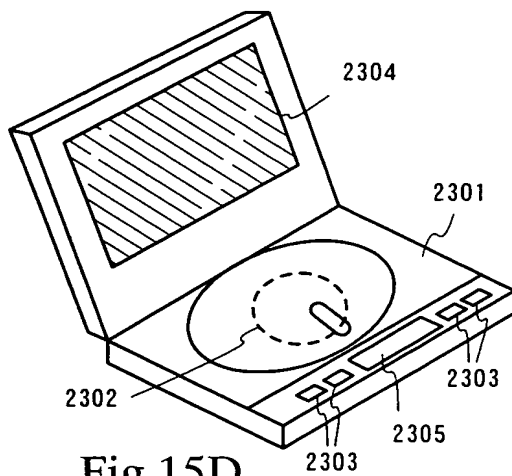


Fig.15D

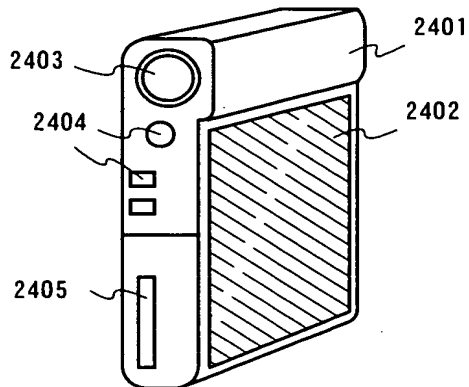


Fig.15E

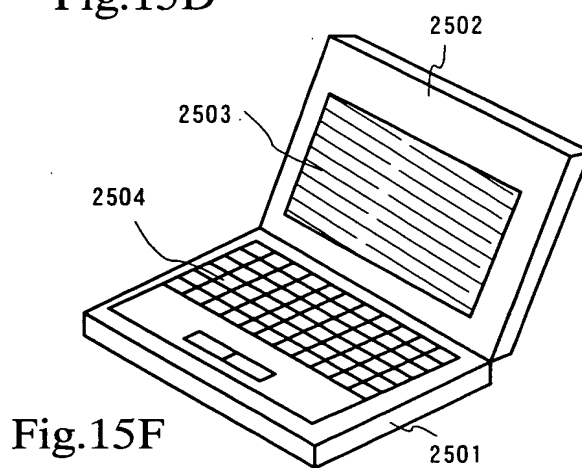


Fig.15F

Fig.16A

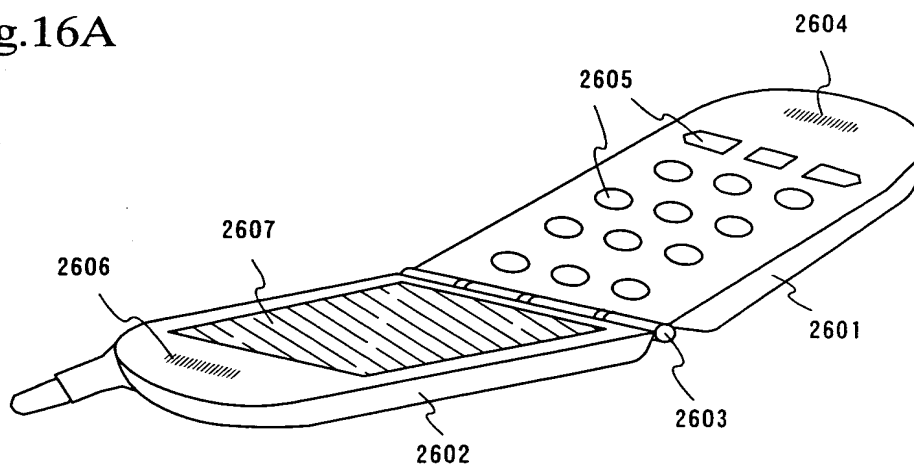


Fig.16B

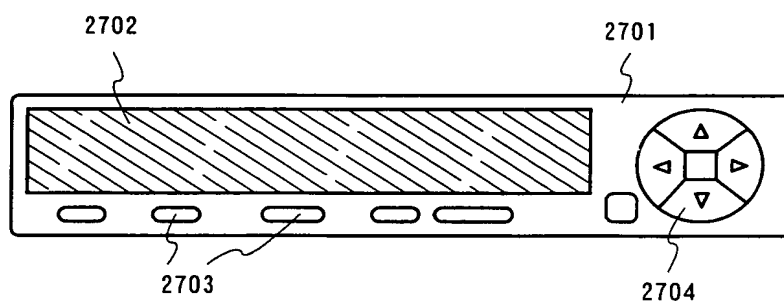




Fig. 17

